



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

\$2825

11/EOT  
8/14/01  
A. Wake

Applicant: Hutter et al.

Art Unit: 2825

Serial No.: 09/276,780

Examiner: Luu, C.

Filing Date: 03/25/99

Docket No.: TI-27203

Title: MERGED BIPOLAR AND CMOS CIRCUIT AND METHOD

EXTENSION OF TIME

Assistant Commissioner  
for Patents  
Washington, DC 20231

Dear Sir:

<b>MAILING CERTIFICATE</b>	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.	
Date	<u>8-2-01</u>
	<u>Marianna Smith</u>
Marianna Smith	

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

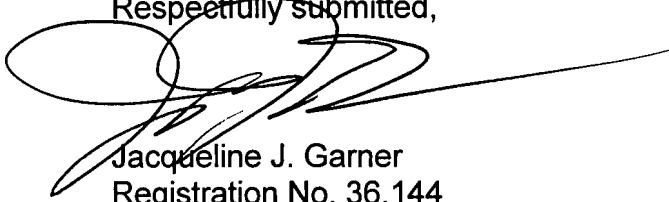
<u>  X  </u>	One Month (\$110)
<u>      </u>	Two Months (\$390)
<u>      </u>	Three Months (\$890)
<u>      </u>	Four Months (\$1390)

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Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated  
P.O. Box 655474, M/S 3999  
Dallas, TX 75265  
(972) 271-1176  
Fax: (972) 917-4418

Respectfully submitted,

  
Jacqueline J. Garner  
Registration No. 36,144  
Attorney for Applicants

08/09/2001 BNGUYEN1 00000092 200668 09276780

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